priority tag and for adding the recalculated FCS to the stripped frame with the priority tag.

10. (New) The system of claim 9, wherein an asserted first signal to the first logic block indicates that the LARQ header is enabled and must be stripped from the frame with the priority tag.

11. (New) The system of claim 9, wherein the first logic block asserts a second signal and a third signal to the second logic block, wherein the second signal indicates that the FCS is to be stripped from the frame with the priority tag, wherein the third signal indicates that the LARQ header is to be stripped from the frame with the priority tag.

12. (New) The system of claim 9, wherein an asserted fourth signal to the third logic block enables the recalculation of the FCS.